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Notice of Allowability	Application No.	Applicant(s)
	10/616,272	CHONG ET AL.
	Examiner	Art Unit
	Daniel D. Chang	2819
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. X This communication is responsive to Response to Restriction Requirement filed December 9, 2004.		
2. The allowed claim(s) is/are <u>1-15,20-24 and 27</u> .		
3. The drawings filed on are accepted by the Examiner.		
4.		
 Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/O Paper No./Mail Date 12/19/03, 7/9/03 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material 	6. ☐ Interview Summa Paper No./Mail D 08), 7. ⊠ Examiner's Amen	Date
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Drawings

The drawings are objected to because of the following: the ground symbol should be removed at the bottom of right side of Fig. 7 (right above where "FIG.7" is labeled); also, a ground symbol should be added at the bottom of Fig. 8, preferably at the source of NMOS transistor $W_{N3}+2_{a3}$.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. The objection to the drawings will not be held in abeyance.

Acknowledgement

Receipt is acknowledged of the Response to Restriction Requirement filed December 9, 2004.

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Reasons for Allowance

Claims 1-15, 20-24, and 27 are allowable over the prior art of record.

The following is an examiner's statement of reasons for allowance: the best prior art of record, Lee et al., taken alone or in combination of other references, does not teach or fairly suggest an output driver circuit comprising, among other things, a resistive element having a first terminal that is electrically coupled to drain terminals of the at least one PMOS pull-up transistor and the at least one NMOS pull-down transistor and a second terminal that is electrically coupled to source terminals of the at least one NMOS pull-up transistor and the at least one PMOS pull-down transistor; or at least one PMOS pull-up transistor comprises a coarsely tuned array of binary weighted PMOS pull-up transistors in parallel with a finely tuned array of non-binary weighted PMOS pull-up transistors; and at least one NMOS pull-down transistor comprises a coarsely tuned array of binary weighted NMOS pull-down transistors in parallel with a finely tuned array of non-binary weighted NMOS pull-down transistors, as set forth in the claims.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Daniel D. Chang whose telephone number is (571) 272-1801. The examiner can normally be reached on Monday through Friday.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael J. Tokar can be reached on (571) 272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Daniel D. Chang Primary Examiner Art Unit 2819

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DANIEL CHANG PRIMARY EXAMINER